

$$\text{exp} ::= \underline{\text{int}} \mid (\underline{\text{read}}) \mid (- \text{exp}) \mid (+ \text{exp} \text{ exp}) \mid \underline{\text{var}} \mid \underline{(\text{let} ([\text{var} \text{ exp}]) \text{exp})}$$

$$R_1 ::= \text{exp}$$

concrete
Syn

$$(\text{let} ([x \text{ (assign } (+ 10 20))]) \text{ (body } (+ x 30))) \Rightarrow \underline{\underline{60}}$$

$x = (+ 10 20)$

$(+ x 30) \Rightarrow (+ 30 30)$

$(\text{let} [\underline{\text{var}} \text{ RHS}] \text{body})$

$$\left. \begin{aligned} &(\text{let} [x (+ 10 20)] x) \\ &(+ x 10) \end{aligned} \right\}$$

$$(\text{let} [x_1 (+ 10 20)] (+ (\text{let} [x_2 (+ 2 3)] x_2) (+ 2 x_1)))$$

$x = 10 + 20$

$\hookrightarrow x = 2 + 3$

$\hookrightarrow \cancel{x} \cancel{x} \cancel{x} 5 + x$

$\hookrightarrow 5 + 30$

$\hookrightarrow 35$

A.S. R0

$$\text{exp} ::= (\text{Int int}) \mid (\text{Prim} \text{ read } (r)) \mid (\text{Prim} \text{ ' - exp}) \mid (\text{Prim} \text{ ' + (exp exp)})$$

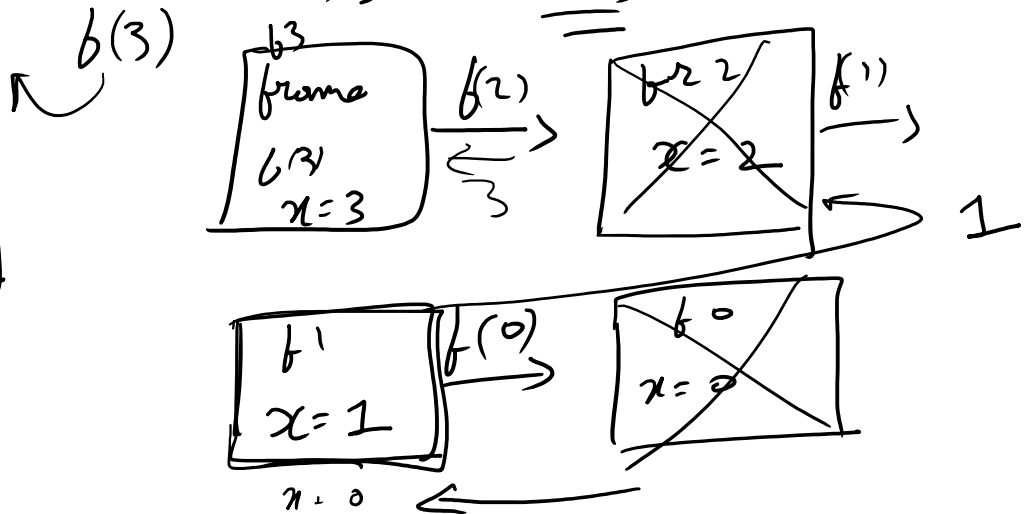
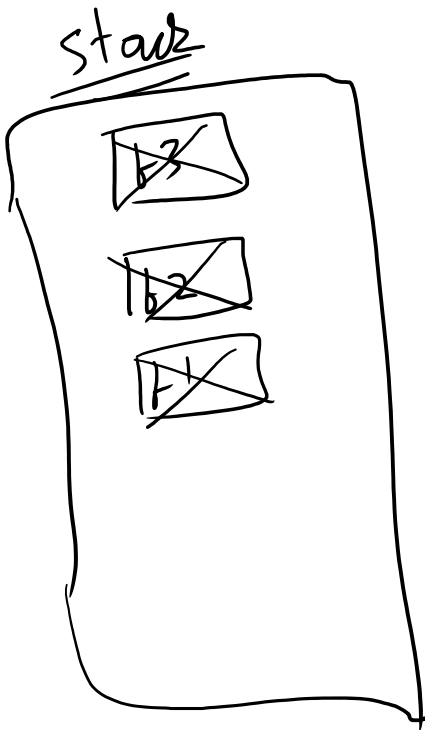
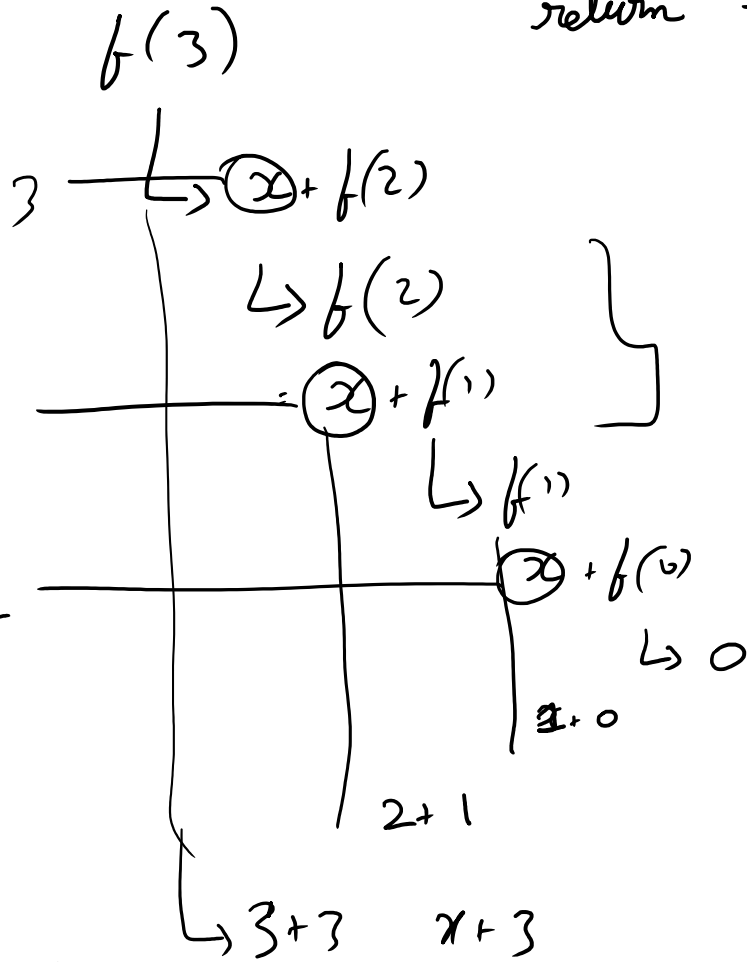
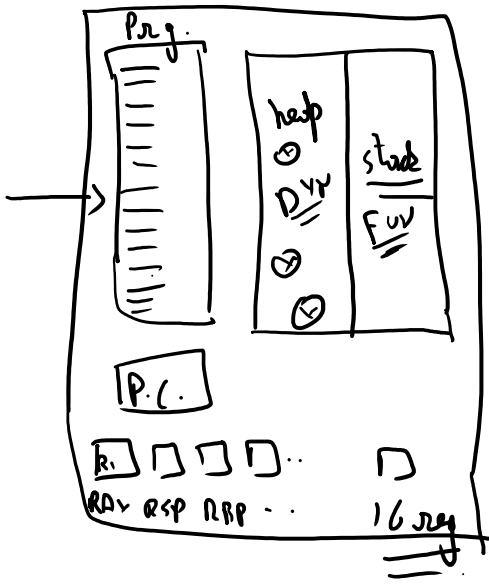
~~(Var var)~~

$$(\text{Var var}) \mid (\text{let var exp exp})$$

10 Bytes

```

int f ( int x ) {
    if ( x == 0 ) {
        return 0;
    }
    else
        return x + f(x-1);
}
    
```



C, P, RA
P implicit

↳ implicit

x86 → partially implicit }

\$8, \$32 (32)

reg ::= rsp | rax | rbp ... | r 15

%rax %rdi

arg ::= \$int | %reg | int (%reg) -8(%rbp)

instr ::= addq arg, arg | dest = src + dest mem

negq arg | dest = -dest

movq arg, arg -> movq %rax, %rdi; %rax -8(%rbp)

callq label | -> func.

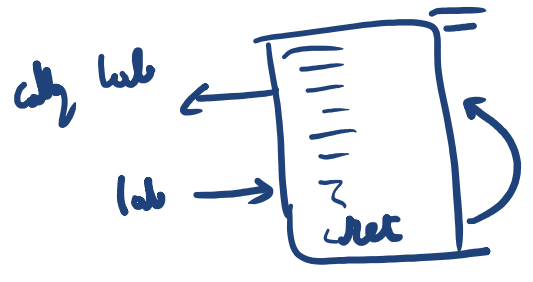
pushq arg | pushq %rax

popq arg | pop %rdi

retq

prog ::= .global main

main: inst +*



(+10 45)

.global main

```

main:
    movq $10, %rax
    movq $45, %rdi
    addq $45, %rax
    movq %rax, %rdi
    call print-int
    movq $0, %rax
    retq

```

Abhinav (written on the left side of the code block)

RAX is return

```

f1:
    movq $10, %rax
    addq $45, %rax
    retq

```

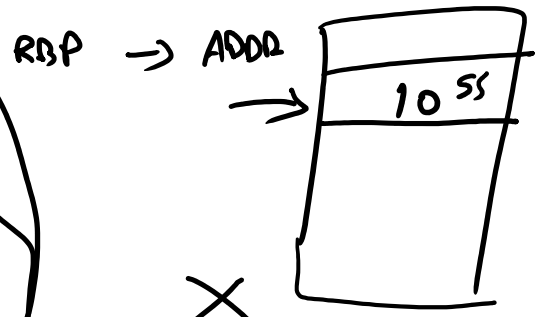
RAX

RBP -> [ADD.R] - 8

```

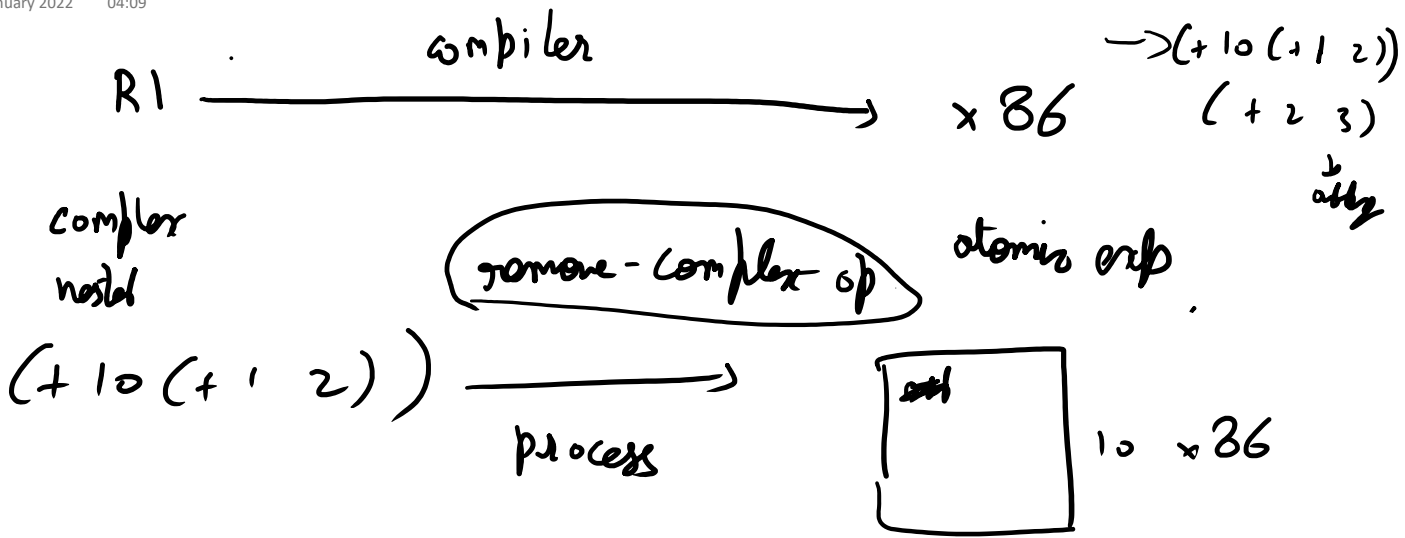
    movq $101, -8(%rbp)
    addq $452, -8(%rbp)
    movq -8(%rbp), (%rax)
    retq

```



X

Regs -> Reg. allocate



Shadows variables
 remark

make unique
 unifying

regs & mem.

error handle

difficult x86

an arb. variable

'assign home'

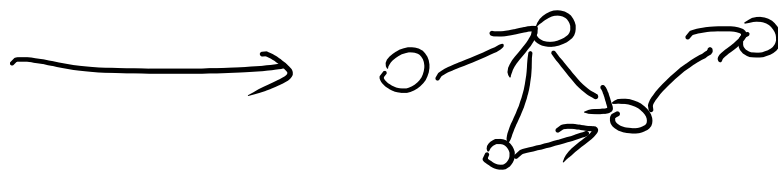
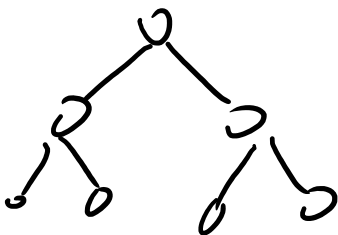
16 regs.
 arb memory

depth first tree

explicit control

seq
 jump

ctrl. flow graph



$(+10 42)$

abg : ram : rlp

(+ 10 42)

↳ SS

2 i/ps

1 o/p

select inst

compiler

abbg %ram %r2lp

abbg sr. bst.

bst updated

R1

↓ unify

R1.1

↓ remove copy op

R1.2

↓ exp. cell

R1.3 $\xrightarrow{\text{sdcl}}$ C1.1 $\xrightarrow{\text{assign}}$ x 36